Future Technology Devices International Ltd

TTL-232R-PCB

TTL to USB Serial Converter PCB

Datasheet

Neither the whole nor any part of the information contained in, or the product described in this manual, may be adapted, or reproduced in any material or electronic form without the prior written consent of the copyright holder. This product and its documentation are supplied on an as-is basis and no warranty as to their suitability for any particular purpose is either made or implied. Future Technology Devices International Ltd will not accept any claim for damages howsoever arising as a result of use or failure of this product. Your statutory rights are not affected. This product or any variant of it is not intended for use in any medical appliance, device, or system in which the failure of the product might reasonably be expected to result in personal injury. This document provides preliminary information that may be subject to change without notice. No freedom to use patents or other intellectual property rights is implied by the publication of this document. Future Technology Devices International Ltd, Unit 1, 2 Seaward Place, Centurion Business Park, Glasgow, G41 1HH, United Kingdom. Scotland Registered Number: SC136640
# 1 Description

The **TTL-232R-PCB** is a USB to TTL serial UART converter PCB incorporating FTDI’s FT232RQ USB to Serial UART interface IC device which handles all the USB signalling and protocols. The PCB provides a fast, simple way to connect devices with a TTL level serial interface to USB.

Each TTL-232R_PCB contains a small internal electronic circuit board, utilising the FT232R plus a USB type-A connector. The FT232R datasheet, DS_FT232R, is available at [FTDI website](https://www.ftdichip.com/). The PCB is RoHS compliant at TTL levels of + 5V or + 3.3V.

The PCB is USB powered and USB 2.0 full speed compatible. Each PCB supports a data transfer rate up to 3 Mbaud and supports the FTDIChip-ID™, with a unique USB serial number programmed into the FT232R. This feature can be used to create a security or password protected file transfer access using the PCB. Further information and examples on this feature are available at [FTDI website](https://www.ftdichip.com) under FTDIChip-ID Projects.

The TTL-232R_PCB requires USB drivers, available free from [FTDI website](https://www.ftdichip.com), which are used to make the FT232R on the PCB appear as a virtual COM port (VCP). This then allows the user to communicate with the USB interface via a standard PC serial emulation port (for example TTY). Another FTDI USB driver, the D2XX driver, can also be used with application software to directly access the FT232R on the PCB though a DLL. This is illustrated in the Figure 1.1.

![Virtual COM Port](image1)

**Virtual COM Port**

![Software application access to USB via D2XX](image2)

**Software application access to USB via D2XX**

*Figure 1.1 Using the TTL-232R-PCB*
1.1 Part Number

The following Table 1.1 gives details of the available TTL-232R-PCB.

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>TTL-232R-5V-PCB</td>
<td>USB to UART PCB with selectable +5V TTL level UART signals.</td>
</tr>
<tr>
<td>TTL-232R-3V3-PCB</td>
<td>USB to UART PCB with selectable +3.3V TTL level UART signals.</td>
</tr>
</tbody>
</table>

Table 1.1 TTL-232R-PCB Part Number

1.2 USB Compliant

The TTL-232R-PCB is fully compliant with the USB 2.0 specification.
# Table of Contents

1 Description .................................................................................................................. 2
   1.1 Part Number ........................................................................................................... 3
   1.2 USB Compliant ....................................................................................................... 3

2 Typical Applications .................................................................................................... 5
   2.1 Driver Support ....................................................................................................... 5
   2.2 Features .................................................................................................................. 6

3 Features of FT232R applicable to TTL-232R PCB ............................................. 7

4 TTL-232R-PCB Connector Pin Out and Mechanical details .......................... 8
   4.1 TTL-232R-PCB Signal Descriptions ................................................................. 9
   4.2 TTL-232R-PCB +5V/+3.3V Selection ................................................................. 9
   4.3 TTL-232R-PCB Electrical Parameters .............................................................. 9

5 TTL-232R-PCB Circuit Schematic .......................................................................... 11

6 Contact Information ................................................................................................ 12

Appendix A - PCB EEPROM Configuration .......................................................... 13

Appendix B - List of Figures and Tables ............................................................... 14
   List of Figures ............................................................................................................. 14
   List of Tables .............................................................................................................. 14

Appendix C - Revision History .................................................................................. 15
2 Typical Applications

- USB to Serial TTL Level Converter
- Upgrading Legacy Peripherals to USB
- Interface Microcontroller UART or I/O to USB
- Interface FPGA / PLD to USB
- Replace MAX232 type level shifters allowing for direct connection of products to PC via USB

- USB Instrumentation PC interface
- USB Industrial Control
- USB Software / Hardware Encryption Dongles

2.1 Driver Support

Royalty free VIRTUAL COM PORT (VCP) and D2XX Direct Drivers are available for the following Operating Systems (OS):

- Windows
- Linux
- Mac
- Android (J2xx / D2xx only)

See the following website link for the full driver support list including OS versions and legacy OS.

https://ftdichip.com/drivers/

Virtual COM Port (VCP) drivers cause the USB device to appear as an additional COM port available to the PC. Application software can access the USB device in the same way as it would access a standard COM port.

D2XX Direct Drivers allow direct access to the USB device through a DLL. Application software can access the USB device through a series of DLL function calls. The functions available are listed in the D2XX Programmer’s Guide document which is available from the Documents section of our website. Please also refer to the Installation Guides webpage for details on how to install the drivers.
2.2 Features

- TTL-232R_PCB provides a USB to TTL Serial UART interface.
- On board FT232RQ provides single chip USB to asynchronous serial data transfer interface.
- Entire USB protocol handled by the electronics on the PCB.
- Connect directly to a microcontroller UART or I/O pins.
- UART interface support for 7 or 8 data bits, 1 or 2 stop bits and odd / even / mark / space / no parity.
- Fully assisted hardware (RTS#/CTS#) or X-On / X-Off software handshaking.
- Data transfer rates from 300 baud to 3 Mbaud at TTL levels.
- Internal EEPROM with user writeable area.
- Selectable +3.3V or +5V CMOS drive outputs and 5V safe TTL inputs makes the TTL-232R_PCB easy to interface to 5V MCU's.
- FTDI’s royalty-free VCP allow for communication as a standard emulated COM port and D2XX ‘direct’ drivers provide DLL application programming interface.
- Support for FT232R FTDIChip-ID™ feature for improved security.
- PCB is USB Powered - no external supply required.
- +5V or +3.3V output allows external logic to be powered from the USB port.
- 6 outputs provide Tx, Rx, RTS#, CTS#, VCC and GND.
- Low USB bandwidth consumption.
- UHCI / OHCI / EHCI host controller compatible.
- USB 2.0 Full Speed compatible.
- -40°C to +85°C operating temperature range.
3 Features of FT232R applicable to TTL-232R PCB

The TTL-232R_PCB uses FTDI’s FT232RQ USB to serial IC device. This section summarises the key features of the FT232RQ which apply to the TTL-232R_PCB USB to serial TTL converter. For further details, and a full features and enhancements description consult the FT232R datasheet, this is available from www.ftdichip.com.

**Internal EEPROM.** The internal EEPROM is used to store USB Vendor ID (VID), Product ID (PID), device serial number, product description string and various other USB configuration descriptors. Each FT232RQ is supplied with the internal EEPROM pre-programmed as described in Appendix A - PCB EEPROM Configuration. A user area of the internal EEPROM is available to system designers to allow storing additional data. The internal EEPROM descriptors can be programmed in circuit, over USB without any additional voltage requirement. It can be programmed using the FTDI utility software called FT_PROG, which can be downloaded from FTDI Utilities on the FTDI website.

**Lower Operating and Suspend Current.** The FT232R has a low 15mA operating supply current and a very low USB suspend current of approximately 70μA.

**Low USB Bandwidth Consumption.** The USB interface of the FT232R, and therefore the TTL-232R_PCB has been designed to use as little as possible of the total USB bandwidth available from the USB host controller.

**High Output Drive Option.** The UART interface I/O pins on the TTL-232R_PCB (RXD, TXD, RTS#, and CTS#) can be configured to use the FT232R’s high output drive option. This option allows the FT232R I/O pins to drive up to three times the standard signal drive level. This allows multiple devices to be driven, or devices that require a greater signal drive strength to be interfaced on the PCB. This option is enabled in the internal EEPROM.

**UART Pin Signal Inversion.** The sense of each of the UART signals can be individually inverted by configuring options in the internal EEPROM. For example, CTS# (active low) can be changed to CTS (active high), or TXD can be changed to TXD#.

**FTDIChip-ID™.** The FT232R includes the new FTDIChip-ID™ security dongle feature. This FTDIChip-ID™ feature allows a unique number to be burnt into each FT_232RQ during manufacture. This number cannot be reprogrammed. This number is only readable over USB can be used to form the basis of a security dongle which can be used to protect any customer application software being copied. This allows the possibility of using the TTL-232R_PCB as a dongle for software licensing. Further to this, a renewable license scheme can be implemented based on the FTDIChip-ID™ number when encrypted with other information. This encrypted number can be stored in the user area of the FT232R internal EEPROM, and can be decrypted, then compared with the protected FTDIChip-ID™ to verify that a license is valid. Web based applications can be used to maintain product licensing this way. An application note, AN232R-02, available from FTDI website describes this feature.

**Extended Operating Temperature Range** - The TTL-232R_PCB is capable of operating over an extended temperature range of -40° to +85°C thus allowing it to be used in automotive or industrial applications.
4 TTL-232R-PCB Connector Pin Out and Mechanical details

The mechanical details of the PCB are shown in the following diagram Figure 4.2.

**TTL232R PCB PADS**

Figure 4.1 TTL-232R-PCB Pin Out (Top is TOP View, Bottom is BOTTOM View)

Dimensions in mm

Figure 4.2 TTL-232R TTL-232R-PCB, Mechanical Details
### 4.1 TTL-232R-PCB Signal Descriptions

<table>
<thead>
<tr>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>GND</td>
<td>Device ground supply pin.</td>
</tr>
<tr>
<td>CTS#</td>
<td>Input</td>
<td>Clear to Send Control input / Handshake signal.</td>
</tr>
<tr>
<td>VCC</td>
<td>Output</td>
<td>+5V output,</td>
</tr>
<tr>
<td>TXD</td>
<td>Output</td>
<td>Transmit Asynchronous Data output.</td>
</tr>
<tr>
<td>RXD</td>
<td>Input</td>
<td>Receive Asynchronous Data input.</td>
</tr>
<tr>
<td>RTS#</td>
<td>Output</td>
<td>Request To Send Control Output / Handshake signal.</td>
</tr>
</tbody>
</table>

Table 4.1 TTL-232R-PCB Signal Descriptions

### 4.2 TTL-232R-PCB +5V/+3.3V Selection

Selection of whether the UART signal are +3.3V levels or +5V levels is done using resistors. The following table gives details of what resistors are required for the two different voltage levels. Note that the VCC output signal will always drive at +5V.

<table>
<thead>
<tr>
<th>Resistor R1</th>
<th>Resistor R2</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fitted</td>
<td>Not Fitted</td>
<td>UART signals are +5V level signals</td>
</tr>
<tr>
<td>Not Fitted</td>
<td>Fitted</td>
<td>UART signals are +3.3V level signals</td>
</tr>
</tbody>
</table>

Table 4.2 UART Signal Level Selection

### 4.3 TTL-232R-PCB Electrical Parameters

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>Output Power Voltage</td>
<td>4.25</td>
<td>5.0</td>
<td>5.25</td>
<td>V</td>
<td>Dependant on the USB port that the TTL-232R-PCB is connected to.</td>
</tr>
<tr>
<td>Io</td>
<td>Output Power Current</td>
<td>-</td>
<td></td>
<td>75</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>T</td>
<td>Operating Temperature Range</td>
<td>-40</td>
<td></td>
<td>+85</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

Table 4.3 TTL-232R-PCB I/O Operating Parameters
### Table 4.4 TTL-232R-PCB I/O Pin Characteristics (+5V level signals)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voh</td>
<td>Output Voltage High</td>
<td>3.2</td>
<td>4.1</td>
<td>4.9</td>
<td>V</td>
<td>I source = 6mA</td>
</tr>
<tr>
<td>Vol</td>
<td>Output Voltage Low</td>
<td>0.3</td>
<td>0.4</td>
<td>0.6</td>
<td>V</td>
<td>I sink = 6mA</td>
</tr>
<tr>
<td>Vin</td>
<td>Input Switching Threshold</td>
<td>1.0</td>
<td>1.2</td>
<td>1.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>VHys</td>
<td>Input Switching Hysteresis</td>
<td>20</td>
<td>25</td>
<td>30</td>
<td>mV</td>
<td></td>
</tr>
</tbody>
</table>

### Table 4.5 TTL-232R-PCB I/O Pin Characteristics (+3.3V level signals)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voh</td>
<td>Output Voltage High</td>
<td>2.2</td>
<td>2.8</td>
<td>3.2</td>
<td>V</td>
<td>I source = 3mA</td>
</tr>
<tr>
<td>Vol</td>
<td>Output Voltage Low</td>
<td>0.3</td>
<td>0.4</td>
<td>0.6</td>
<td>V</td>
<td>I sink = 8mA</td>
</tr>
<tr>
<td>Vin</td>
<td>Input Switching Threshold</td>
<td>1.0</td>
<td>1.2</td>
<td>1.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>VHys</td>
<td>Input Switching Hysteresis</td>
<td>20</td>
<td>25</td>
<td>30</td>
<td>mV</td>
<td></td>
</tr>
</tbody>
</table>
The circuit schematic of the TTL-232R-PCB, utilising the FTDI FT232R, is shown in Figure 5.1.

Figure 5.1 Circuit Schematic of TTL-232R-PCB.
6 Contact Information

Head Office – Glasgow, UK
Future Technology Devices International Limited (UK)
Unit 1, 2 Seaward Place, Centurion Business Park
Glasgow G41 1HH
United Kingdom
Tel: +44 (0) 141 429 2777
Fax: +44 (0) 141 429 2758
E-mail (Sales) sales1@ftdichip.com
E-mail (Support) support1@ftdichip.com
E-mail (General Enquiries) admin1@ftdichip.com

Branch Office – Tigard, Oregon, USA
Future Technology Devices International Limited (USA)
7130 SW Fir Loop
Tigard, OR 97223-8160
USA
Tel: +1 (503) 547 0988
Fax: +1 (503) 547 0987
E-mail (Sales) us.sales@ftdichip.com
E-mail (Support) us.support@ftdichip.com
E-mail (General Enquiries) us.admin@ftdichip.com

Branch Office – Taipei, Taiwan
Future Technology Devices International Limited (Taiwan)
2F, No. 516, Sec. 1, NeiHu Road
Taipei 114
Taiwan, R.O.C.
Tel: +886 (0) 2 8797 1330
Fax: +886 (0) 2 8751 9737
E-mail (Sales) tw.sales1@ftdichip.com
E-mail (Support) tw.support1@ftdichip.com
E-mail (General Enquiries) tw.admin1@ftdichip.com

Branch Office – Shanghai, China
Future Technology Devices International Limited (China)
Room 1103, No. 666 West Huaihai Road,
Shanghai, 200052
China
Tel: +86 (21) 62351596
Fax: +86 (21) 62351595
E-mail (Sales) cn.sales@ftdichip.com
E-mail (Support) cn.support@ftdichip.com
E-mail (General Enquiries) cn.admin@ftdichip.com

Web Site
https://ftdichip.com/

Distributor and Sales Representatives
Please visit the Sales Network page of the FTDI Web site for the contact details of our distributor(s) and sales representative(s) in your country.
Appendix A - PCB EEPROM Configuration

Each TTL-232R-PCB is controlled by the FTDI FT232R IC. This FT232R device contains an EEPROM which contains the USB configuration descriptors for that device. When the PCB is plugged into a PC or a USB reset is performed, the PC will read these descriptors. The default values stored into the internal EEPROM are defined in the table below.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>USB Vendor ID (VID)</td>
<td>0403h</td>
<td>FTDI default VID (hex)</td>
</tr>
<tr>
<td>USB Product UD (PID)</td>
<td>6001h</td>
<td>FTDI default PID (hex)</td>
</tr>
<tr>
<td>Serial Number Enabled?</td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Serial Number</td>
<td>See note</td>
<td>A unique serial number is generated and programmed into the EEPROM during device final test.</td>
</tr>
<tr>
<td>Pull down I/O Pins in USB Suspend</td>
<td>Disabled</td>
<td>Enabling this option will make the device pull down on the UART interface lines when the power is shut off (PWREN# is high).</td>
</tr>
<tr>
<td>Manufacturer Name</td>
<td>FTDI</td>
<td></td>
</tr>
<tr>
<td>Product Description</td>
<td>See note</td>
<td>TTL-232R-PCB</td>
</tr>
<tr>
<td>Max Bus Power Current</td>
<td>90mA</td>
<td></td>
</tr>
<tr>
<td>Power Source</td>
<td>Bus Powered</td>
<td></td>
</tr>
<tr>
<td>Device Type</td>
<td>FT232R</td>
<td></td>
</tr>
<tr>
<td>USB Version</td>
<td>0200</td>
<td>Returns USB 2.0 device description to the host. <strong>Note:</strong> The device is a USB 2.0 Full Speed device (12Mb/s) as opposed to a USB 2.0 High Speed device (480Mb/s).</td>
</tr>
<tr>
<td>Remote Wake Up</td>
<td>Disabled</td>
<td></td>
</tr>
<tr>
<td>High Current I/Os</td>
<td>Enabled</td>
<td>Enables the high drive level on the UART and CBUS I/O pins.</td>
</tr>
<tr>
<td>Load VCP Driver</td>
<td>Enabled</td>
<td>Makes the device load the VCP driver interface for the device.</td>
</tr>
<tr>
<td>Invert TXD</td>
<td>Disabled</td>
<td>Signal on this pin becomes TXD# if enable.</td>
</tr>
<tr>
<td>Invert RXD</td>
<td>Disabled</td>
<td>Signal on this pin becomes RXD# if enable.</td>
</tr>
<tr>
<td>Invert RTS#</td>
<td>Disabled</td>
<td>Signal on this pin becomes RTS if enable.</td>
</tr>
<tr>
<td>Invert CTS#</td>
<td>Disabled</td>
<td>Signal on this pin becomes CTS if enable.</td>
</tr>
</tbody>
</table>

Table 0.1 Default Internal EEPROM Configuration

The internal EEPROM on the PCB can be re-programmed over USB using the utility program [FT_PROG](http://www.ftdichip.com). Version 2.8a or later is required for the FT232R chip. Users who do not have their own USB Vendor ID but who would like to use a unique Product ID in their design can apply to FTDI for a free block of unique PIDs. Contact FTDI support for this service.
Appendix B - List of Figures and Tables

List of Figures

Figure 1.1 Using the TTL-232R-PCB ........................................................................................................... 2
Figure 4.1 TTL-232R-PCB Pin Out (Top is TOP View, Bottom is BOTTOM View)................................. 8
Figure 4.2 TTL-232R TTL-232R-PCB, Mechanical Details ................................................................. 8
Figure 5.1 Circuit Schematic of TTL-232R-PCB .................................................................................. 11

List of Tables

Table 1.1 TTL-232R-PCB Part Number ................................................................................................. 3
Table 4.1 TTL-232R-PCB Signal Descriptions .................................................................................... 9
Table 4.2 UART Signal Level Selection ................................................................................................. 9
Table 4.3 TTL-232R-PCB I/O Operating Parameters ......................................................................... 9
Table 4.4 TTL-232R-PCB I/O Pin Characteristics (+5V level signals) .................................................. 10
Table 4.5 TTL-232R-PCB I/O Pin Characteristics (+3.3V level signals) ............................................. 10
Table 0.1 Default Internal EEPROM Configuration ............................................................................. 13
# Appendix C - Revision History

<table>
<thead>
<tr>
<th>Revision</th>
<th>Changes</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.00</td>
<td>First Release</td>
<td>08-08-2008</td>
</tr>
<tr>
<td>1.01</td>
<td>Added RoHS compliant statement in section 1.</td>
<td>02-09-2010</td>
</tr>
<tr>
<td></td>
<td>Added section 1.2 USB Compliant Logo.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Updated contact details.</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Replaced reference MProg with FT_PROG.</td>
<td></td>
</tr>
<tr>
<td>1.02</td>
<td>Updated Figure 4.2 TTL-232R TTL-232R-PCB, Mechanical Details.</td>
<td>25-10-2023</td>
</tr>
</tbody>
</table>