This Technical Note shows examples of FTDI IC PCB footprints which can be used as a guide for creating your own IC PCB footprints.

Use of FTDI devices in life support and/or safety applications is entirely at the user's risk, and the user agrees to defend, indemnify, and hold FTDI harmless from any and all damages, claims, suits, or expense resulting from such use.
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1 Introduction

This Technical Note shows examples of FTDI IC PCB footprints which can be used as a guide for creating your own PCB footprints.

The IC footprints in this document are sourced from various FTDI hardware such as development and application modules and demo hardware, using the most common and cost-effective package types.

The IC footprints in this document provide:

- A 1:1 scaled IC footprint
- An annotated IC footprint showing some key measurements

All dimensions shown are in millimeters (mm).

Additionally, a range of USB Interface IC solutions from FTDI Chip available through AltiumLive.

To view Altium files, you need either the full version of ‘Altium Designer’, or ‘Altium Viewer’ which can be downloaded for free from Altium’s web site.

Note that all IC footprints may not be available through AltiumLive. Please contact FTDI in this case. We can supply you with Altium PCBLib files.

1.1 Scope

These IC PCB footprints can be used as a guide to create your own IC PCB footprints with particular PCB design tools other than Altium.

Please refer to the IC datasheet for full IC package parameters.

**Note 1:** These footprints are provided as an example only and are not optimized for all soldering processes. Customers must modify the footprint as required to optimize it to match their soldering process.

**Note 2:** No guarantees can be provided in this document. These can be used as a guide only.

**Note 3:** FTDI Cables and Modules are recommended for product test and development prior to custom hardware development.
2 All Scaled Footprints

This section shows all packages scaled to 1:1 size to show the exact package size which can help when selecting a package to use in your design.

Note that not all packages are available for all products. See Section 3 ‘Packages by Product’ in this document, the product datasheet, or check the IC webpage:


2.1 DFN Packages

Figure 2.1 shown in pin count order from left to right:
DFN-10, DFN-12.

![Figure 2.1 DFN Packages](image)

2.2 QFP Packages

Figure 2.2 shown in pin count order from left to right:
LQFP-32, LQFP-48, LQFP-64, TQFP-64.

![Figure 2.2 QFP Packages](image)

2.3 QFN Packages

Figure 2.3 shown in pin count order from left to right:
QFN-16 (4x4), QFN-20, QFN-24, WQFN-28, QFN-32 (5x5), QFN-32 (7x7), QFN-48 (8x8), QFN-56 (7x7), VQFN-56 EP1 (8x8), QFN-56 EP2 (8x8), QFN-64 EP1 (9x9), QFN-64 EP2 (9x9), VQFN-64 (9x9), QFN-68 (8x8), QFN-76 EP1 (9x9), QFN-76 EP2 (9x9).

![Figure 2.3 QFN Packages](image)

2.3.1 QFN Exposed Pads

Please note that there are different footprints with respect to the exposed pads on the QFN packages.

With “centralized” exposed pads, the solder is centralizing due to surface tension and may weaken the heat dissipation along the corners. This works when the thermal pad is reasonably bigger. See Figure 2.4.

![Figure 2.4 Centralized Exposed Pad](image)
With “braced” exposed pads, they have the benefit of preventing solder bridging. There are 4 points to centralize the solder, which makes better bonding and heat dissipation. This also uses less solders paste and less heat up rate required. See Figure 2.5.

**Figure 2.5 Braced Exposed Pad**

The final QFN soldering quality is largely affected by how the PCB assembly house control their process.

### 2.4 SSOP Packages

Figure 2.6 shown in pin count order from left to right:


**Figure 2.6 SSOP Packages**
3 Packages by Product

Package availability for FTDI products is shown in this section.

3.1 DFN Packages

<table>
<thead>
<tr>
<th>Package</th>
<th>Part Numbers</th>
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<tbody>
<tr>
<td>DFN-10</td>
<td>FT200XD</td>
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<tr>
<td>DFN-12</td>
<td>FT234XD</td>
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Table 3.1 DFN Packages

3.2 QFP Packages

<table>
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<tr>
<th>Package</th>
<th>Part Numbers</th>
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<tbody>
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<td>LQFP-32</td>
<td>FT232BL, FT245BL, FT311D-32L1C, FT312D-32L1C, VNC2-32L1B</td>
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<tr>
<td>LQFP-48</td>
<td>FT232HL, FT2232D, VNC1L-1A, VNC2--48L1B</td>
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<td>LQFP-64</td>
<td>FT2232HL, FT4232HL, FT313HL</td>
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<td>TQFP-64</td>
<td>FT313HP</td>
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Table 3.2 QFP Packages

3.3 QFN Packages

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<tr>
<th>Package</th>
<th>Part Numbers</th>
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<tbody>
<tr>
<td>QFN-16</td>
<td>FT201XQ, FT220XQ, FT230XQ</td>
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<tr>
<td>QFN-20</td>
<td>FT221XQ, FT231XQ</td>
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<td>QFN-24</td>
<td>FT240XQ</td>
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<td>WQFN-28</td>
<td>FT260Q</td>
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Table 3.3 QFN Packages

3.4 SSOP Packages

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<td>FT221XS, FT231XS</td>
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<td>SSOP-28</td>
<td>FT232RL, FT232RL, FT245RL, FT245RL, FT245RL</td>
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<tr>
<td>TSSOP-28</td>
<td>FT120T, FT260S</td>
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</tbody>
</table>

Table 3.4 SSOP Packages
4 10-pin DFN

The 10-pin DFN is used on the following product:

- **FT200XD**

This package is nominally 3.00mm x 3.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

4.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

*Figure 4.1 10-pin DFN Scaled Footprint*

4.2 Annotated Footprint

The annotated footprint shows key measurements.

*Figure 4.2 10-pin DFN Annotated Footprint*

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
5 12-pin DFN

The 12-pin DFN is used on the following product:

- FT234XD

This package is nominally 3.00mm x 3.00mm. The solder pads are on a 0.45mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

5.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 5.1 12-pin DFN Scaled Footprint](image)

5.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 5.2 12-pin DFN Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
6 16-pin QFN (4mm x 4mm)

The 16-pin QFN (4mm x 4mm) is used on the following products:

- FT201XQ
- FT220XQ
- FT230XQ

This package is nominally 4.00mm x 4.00mm. The solder pads are on a 0.65mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

6.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

Figure 6.1 16-pin QFN (4mm x 4mm) Scaled Footprint

6.2 Annotated Footprint

The annotated footprint shows key measurements.

Figure 6.2 16-pin QFN (4mm x 4mm) Annotated Footprint

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
7 16-pin SSOP

The 16-pin SSOP is used on the following products:

- FT201XS
- FT220XS
- FT230XS

This package is nominally 4.90mm x 3.91mm body (4.90mm x 5.99mm including pins). The solder pads are on a 0.635mm pitch.

Please see the IC Package Parameters in the IC datasheet for full information.

7.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 7.1 16-pin SSOP Scaled Footprint](image)

7.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 7.2 16-pin SSOP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
8 20-pin QFN

The 20-pin QFN is used on the following products:

- FT221XQ
- FT231XQ

This package is nominally 4.00mm x 4.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

8.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 8.1 20-pin QFN Scaled Footprint](image)

8.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 8.2 20-pin QFN Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
9 20-pin SSOP

The 20-pin SSOP is used on the following products:

- FT221XS
- FT231XS

This package is nominally 8.66mm x 3.91mm body (8.66mm x 5.99mm including pins). The solder pads are on a 0.635mm pitch.

Please see the IC Package Parameters in the IC datasheet for full information.

9.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 9.1 20-pin SSOP Scaled Footprint](image)

9.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 9.2 20-pin SSOP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
10 24-pin QFN

The 24-pin QFN is used on the following product:

- **FT240XQ**

This package is nominally 4.00mm x 4.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

10.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 10.1 24-pin QFN Scaled Footprint](image)

10.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 10.2 24-pin QFN Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
11 24-pin SSOP

The 24-pin SSOP is used on the following products:

- **FT240XS**

This package is nominally 8.66mm x 3.91 mm body (8.66mm x 5.99mm including pins). The solder pads are on a 0.635mm pitch.

Please see the IC Package Parameters in the IC datasheet for full information.

11.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 11.1 24-pin SSOP Scaled Footprint](image)

11.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 11.2 24-pin SSOP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
12 28-pin SSOP

The 28-pin SSOP is used on the following products:

- FT232RL
- FT232RNL
- FT245RL
- FT245RNL

This package is nominally 5.30mm x 10.20mm body (7.80mm x 10.20mm including pins). The solder pads are on a 0.65mm pitch.

Please see the IC Package Parameters in the IC datasheet for full information.

12.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

Figure 12.1 28-pin SSOP Scaled Footprint

12.2 Annotated Footprint

The annotated footprint shows key measurements.

Figure 12.2 28-pin SSOP Annotated Footprint

**Note:** Red = top layer copper, other colors are mechanical layers
13 28-pin TSSOP

The 28-pin TSSOP is used on the following products:

- FT120T
- FT260S

This package is nominally 9.7mm x 4.4mm body (9.7mm x 6.4mm including pins). The solder pads are on a 0.65mm pitch.

Please see the IC Package Parameters in the IC datasheet for full information.

13.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 13.1 28-pin TSSOP Scaled Footprint](image)

13.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 13.2 28-pin TSSOP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
14  28-pin WQFN

The 28-pin WQFN is used on the following product:

- FT260Q

This package is nominally 5.00mm x 5.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

14.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 14.1 28-pin WQFN Scaled Footprint](image)

14.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 14.2 28-pin WQFN Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
15 32-pin LQFP

The 32-pin LQFP is used on the following products:

- VNC2-32L1C
- FT311D-32L1C
- FT312D-32L1C
- FT232BL
- FT245BL

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.80mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

15.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 15.1 32-pin LQFP Scaled Footprint](image)

15.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 15.2 32-pin LQFP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
16 32-pin VQFN/QFN (5mm x 5mm)

The 32-pin VQFN/QFN (5mm x 5mm) is used on the following products:

- FT232RQ
- FT232RNQ
- FT245RQ
- FT245RNQ
- FT4222HQ

This package is nominally 5.00mm x 5.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

16.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Scaled Footprint](image1)

**Figure 16.1 32-pin VQFN/QFN (5mm x 5mm) Scaled Footprint**

16.2 Annotated Footprint

The annotated footprint shows key measurements.

![Annotated Footprint](image2)

**Figure 16.2 32-pin VQFN/QFN (5mm x 5mm) Annotated Footprint**

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
17  32-pin QFN (7mm x 7mm)

The 32-pin QFN (7mm x 7mm) is used on the following products:

- VNC2-32Q1C
- FT311D-32Q1C
- FT312D-32Q1C

This package is nominally 7.00mm x 7.00mm. The solder pads are on a 0.65mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

17.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 17.1 32-pin QFN (7mm x 7mm) Scaled Footprint](image)

17.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 17.2 32-pin QFN (7mm x 7mm) Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
18 48-pin LQFP

The 48-pin LQFP is used on the following products:

- FT232HL
- FT2232D
- VNC1L-1A
- VNC2-48L1C

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

### 18.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 18.1 48-pin LQFP Scaled Footprint](image)

### 18.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 18.2 48-pin LQFP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
19 48-pin QFN (8mm x 8mm)

The 48-pin QFN (8mm x 8mm) is used on the following products:

- VNC2-48Q1C
- FT232HQ

This package is nominally 8.00mm x 8.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

19.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 19.1 48-pin QFN (8mm x 8mm) Scaled Footprint](image1)

19.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 19.2 48-pin QFN (8mm x 8mm) Annotated Footprint](image2)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
20 56-pin QFN (7mm x 7mm)

The 56-pin QFN (7mm x 7mm) is used on the following product:

- FT600Q-B

This package is nominally 7.00mm x 7.00mm. The solder pads are on a 0.40mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

20.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 20.1 56-pin QFN (7mm x 7mm) Scaled Footprint](image)

20.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 20.2 56-pin QFN (7mm x 7mm) Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area. Cross-hatching designs used for less solder paste and less heat up rate required.
21 56-pin VQFN EP1 (8mm x 8mm)

The 56-pin VQFN EP1 (8mm x 8mm) is used on the following products:

- FT2232H-56Q
- FT4232H-56Q

This package is nominally 8.00mm x 8.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

21.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 21.1 56-pin VQFN EP1 (8mm x 8mm) Scaled Footprint](image1)

21.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 21.2 56-pin VQFN EP1 (8mm x 8mm) Annotated Footprint](image2)

Note 1: Red = top layer copper, other colors are mechanical layers.

Note 2: Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
22 56-pin QFN EP2 (8mm x 8mm)

The 56-pin QFN EP2 (8mm x 8mm) is used on the following products:

- **FT232HPQ**

This package is nominally 8.00mm x 8.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

### 22.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 22.1 56-pin QFN EP2 (8mm x 8mm) Scaled Footprint](image)

### 22.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 22.2 56-pin QFN EP2 (8mm x 8mm) Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
23 64-pin LQFP

The 64-pin LQFP is used on the following products:

- FT313HL
- FT2232HL
- FT4232HL

This package is nominally 12.00mm x 12.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

23.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

Figure 23.1 64-pin LQFP Scaled Footprint

23.2 Annotated Footprint

The annotated footprint shows key measurements.

Note: Red = top layer copper, other colors are mechanical layers.
24 64-pin TQFP

The 64-pin TQFP is used on the following products:

- FT313HP

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.40mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

24.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 24.1 64-pin TQFP Scaled Footprint](image)

24.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 24.2 64-pin TQFP Annotated Footprint](image)

**Note:** Red = top layer copper, other colors are mechanical layers.
25 64-pin QFN EP1 (9mm x 9mm)

The 64-pin QFN EP1 (9mm x 9mm) is used on the following products:

- FT313HQ
- FT2232HQ
- FT4232HQ

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

25.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 25.1 64-pin QFN EP1 (9mm x 9mm) Scaled Footprint](image)

25.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 25.2 64-pin QFN EP1 (9mm x 9mm) Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
26 64-pin QFN EP2 (9mm x 9mm)

The 64-pin QFN EP2 (9mm x 9mm) is used on the following products:

- FT233HPQ

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

26.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

Figure 26.1 64-pin QFN EP2 (9mm x 9mm) Scaled Footprint

26.2 Annotated Footprint

The annotated footprint shows key measurements.

Note 1: Red = top layer copper, other colors are mechanical layers.

Note 2: Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
27 64-pin VQFN (9mm x 9mm)

The 64-pin VQFN (9mm x 9mm) is used on the following products:

- FT4232HAQ

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

27.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 27.1 64-pin VQFN (9mm x 9mm) Scaled Footprint](image)

27.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 27.2 64-pin VQFN (9mm x 9mm) Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
28 68-pin QFN (8mm x 8mm)

The 68-pin QFN (8mm x 8mm) is used on the following products:

- FT2232HPQ
- FT4232HPQ

This package is nominally 8.00mm x 8.00mm. The solder pads are on a 0.50mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

28.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 28.1 68-pin QFN (8mm x 8mm) Scaled Footprint](image1)

28.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 28.2 68-pin QFN (8mm x 8mm) Annotated Footprint](image2)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area.
29  76-pin QFN EP1 (9mm x 9mm)

The 76-pin QFN EP1 is used on the following products:

- FT601Q-B
- FT602Q-B

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.40mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

29.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

Figure 29.1 76-pin QFN EP1 Scaled Footprint

29.2 Annotated Footprint

The annotated footprint shows key measurements.

Figure 29.2 76-pin QFN EP1 Annotated Footprint

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area. Cross-hatching design used for less solders paste and less heat up rate required.
30  76-pin QFN EP2 (9mm x 9mm)

The 76-pin QFN EP2 is used on the following products:

- FT2233HPQ
- FT423HPQ

This package is nominally 9.00mm x 9.00mm. The solder pads are on a 0.40mm pitch. Please see the IC Package Parameters in the IC datasheet for full information.

30.1 Scaled Footprint

This 1:1 scaled footprint is the exact size when viewed or printed at 100%.

![Figure 30.1 76-pin QFN EP2 Scaled Footprint](image)

30.2 Annotated Footprint

The annotated footprint shows key measurements.

![Figure 30.2 76-pin QFN EP2 Annotated Footprint](image)

**Note 1:** Red = top layer copper, other colors are mechanical layers.

**Note 2:** Connect exposed center pad to GND. Do not place tracks on the top layer of the PCB in this area. Cross-hatching design used for less solders paste and less heat up rate required.
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## Appendix A – References

### Document References

http://www.ftdichip.com/Products/ICs.htm

### Acronyms and Abbreviations

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<td>Dual-Flat No-Leads Package</td>
</tr>
<tr>
<td>IC</td>
<td>Integrated Circuit</td>
</tr>
<tr>
<td>LQFP</td>
<td>Low Profile Quad Flat Package</td>
</tr>
<tr>
<td>PCB</td>
<td>Printed Circuit Board</td>
</tr>
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<td>Initial Release</td>
<td>27-04-2016</td>
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<tr>
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<td>Updated Release</td>
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<td>1.2</td>
<td>Removed discontinued parts (VNC2-64, FT121, FT122, FT51A)</td>
<td>14-06-2019</td>
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<td></td>
<td>Removed Bridgetek parts as they are now covered in a separate document.</td>
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<td></td>
<td>Added FT602.</td>
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<td></td>
<td>Edited FT2232H-56Q and FT4232H-56Q part numbers.</td>
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<td></td>
<td>Added new footprints for SSOP28 (FT232RL, FT245RL), 56-pin VQFN (FT2232H-56Q, FT4232H-56Q)</td>
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<td>1.3</td>
<td>Note added to section 1.1 regarding optimization for soldering processes.</td>
<td>12-10-2021</td>
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<td>Added FT232HPQ, FT233HPQ, FT2232HPQ, FT2233HPQ, FT4232HPQ, FT4233HPQ PCB footprints.</td>
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<td></td>
<td>Added section 2.3.1 QFN Exposed Pads.</td>
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<td>1.4</td>
<td>Added FT4232HAQ PCB footprint</td>
<td>23-02-2022</td>
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<tr>
<td>1.5</td>
<td>Added FT232RNQ, FT245RNQ, FT232RNL and FT245RNL.</td>
<td>08-08-2022</td>
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<td>Removed FT120Q (EOL).</td>
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