The intention of this errata technical note is to give a detailed description of known functional or electrical issues with the FTDI FT2232D devices. The current revision of the FT2232D is revision A, released June 2006.
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1 FT2232D Revision

FT2232D part numbers are listed in Table 1. The letter at the end of date code identifies the device revision.

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>FT2232D</td>
<td>48 Pin LQFP</td>
</tr>
</tbody>
</table>

Table 1 FT2232D Part Numbers

This errata technical note covers the revisions of FT2232D listed in Table 2.

<table>
<thead>
<tr>
<th>Revision</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>-</td>
<td>First device revision</td>
</tr>
</tbody>
</table>

Table 2 FT2232D Revisions
## 2 Errata History Table – Functional Problems

<table>
<thead>
<tr>
<th>Functional Problem</th>
<th>Short description</th>
<th>Errata occurs in device revision</th>
</tr>
</thead>
<tbody>
<tr>
<td>FT2232D</td>
<td>Bitbang pulse width not stable</td>
<td>-</td>
</tr>
</tbody>
</table>

### 2.1 Errata History Table – Electrical and Timing Specification Deviations.

<table>
<thead>
<tr>
<th>Deviations</th>
<th>Short description</th>
<th>Errata occurs in device revision</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
3 Functional Problems of FT2232D

3.1 Revision A

3.1.1 BitBang Mode variable Pulse Width

Introduction:

BitBang is a mode the device may be put into to allow free running data to be clocked in/out of the device without any control bits.

Problem:

The output may be clocked out at different speeds to allow for different pulse widths. However this clocking stage is not synchronized with the incoming data and can result in the pulse widths varying unexpectedly on the output.

Workaround:

Set the clock divisor to 1 (baud rate = 3,000,000) and pad the data field with extra 1's or 0's to achieve the required pulse width for each bit.

Package specific:

The effected packages are listed in Table 3

<table>
<thead>
<tr>
<th>Package</th>
<th>Applicable (Yes/No)</th>
</tr>
</thead>
<tbody>
<tr>
<td>FT2232D</td>
<td>Y</td>
</tr>
</tbody>
</table>

Table 3
4  Electrical and Timing specification deviations of FT2232D

None at this time.
5 FT2232D Package Markings

FT2232D is available in a RoHS Compliant package, 48 pin LQFP. An example of the markings on the package is shown in Figure 5-1.

\[ XXYY = \text{Date Code where XX = 2 digit year number, YY = 2 digit week number}; \]

![Figure 5-1 Package Markings – FT2232D](image-url)
6 Contact Information

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### Appendix C – Revision History

<table>
<thead>
<tr>
<th>Version</th>
<th>Draft</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>Draft</td>
<td>First Draft</td>
<td>04/05/2010</td>
</tr>
<tr>
<td>Version 1.0</td>
<td>First Release</td>
<td>22/11/2010</td>
</tr>
</tbody>
</table>