

Future Technology Devices International Limited Practical USB Interface Solutions

Unit1, 2 Seaward Place, Centurion Business Park, Glasgow G41 1HH, Scotland, United Kingdom

Tel: +44 (0)141 429 2777 Fax: +44 (0) 141 429 2758 E-Mail: sales1@ftdichip.com Web: http://www.ftdichip.com

Product Change Notification

Floudet change Notification		
Product Part Number	FT-X series of ICs: FT200XD, FT201XS, FT201XQ, FT220XS, FT220XQ, FT221XS, FT221XQ, FT230XS, FT230XQ, FT231XS, FT231XQ, FT240XS, FT240XQ.	Implementation date
Description of Change	FT-X series of devices revised from Revision C to Revision D	31 October 2012
Reason for Change	FT-X Revision C devices exhibited 2 issues; 1)Extremely rare inoperability based upon payload contents, and 2) Full operation with one specific USB3 host. These issues are discussed in full in FTDI Technical Notes errata, TN_135 to TN_141. These can be found on the following FTDI webpage: http://www.ftdichip.com/Support/Documents/TechnicalNotes.htm	
Detailed Description	Refer to the Technical Note errata above.	
Impact to Data sheet	Datasheets do not change. Change of Rev C to Rev D devices will be recorded in the Technical Note errata indicated above.	
Benefit of Change	No issue with logical zeros USB payload. No issues with USB 3.0 hosts.	
Markings to distinguish revised from:	Device date-code will be changed to a "D" to indicate Revision D. The date-code marked on the devices will change from YYWW-C to YYWW-D (where YY= year and WW= week of manufacture)	
Sample Availability	September 2012	
Risk Assessment, Fit Form and Function & reliability	Risk assessment: no risk of operation Fit: package dimensions are unchanged. Form: visual appearance is unchanged except for markings. Function: FT-X series Rev D will not enter standby due to payload pattern. It will not have an issue with particular USB 3.0 Hosts.	
PDF Download	http://www.ftdichip.com/Support/Documents/ProductChangeNotifications/PCN_FT_012.pdf	

Others:

The latest addition to the FT-X family of devices, the FT234XD, is not affected. It will be introduced using Rev D silicon.