

## **Future Technology Devices International Ltd.**

# **Application Note**

# AN\_163

# **Vinculum-II USB Slave Disconnect Detection**

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This application note provides an example of how to detect when an FTDI Vinculum-II (VNC2) USB Slave device has been disconnected from USB. Sample source code is included for reference.

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## 1 Introduction

This application note describes how an application can detect that a Vinculum-II USB slave device has been disconnected from USB.

The sample source code for the application is provided as an example and is neither guaranteed nor supported by FTDI.

### **1.1 Overview**

The method involves connecting the 5V bus power from the USB Slave to a GPIO input, and implementing an application thread to monitor the GPIO input.

The remainder of this application note describes a specific implementation of this method.

### **1.2 Hardware Requirements**

- V2EVAL board
- 64-pin Vinculum II daughter board

See Appendix A for references to the datasheets.



## 2 Implementation

This section describes a specific implementation of disconnect detection. The reader should have a sound understanding of firmware application structure and the fundamental concepts of how the Vinculum Operating System (VOS) works. For an introduction to VinIDE, VOS and an overview of the general application structure please refer to *Vinculum-II\_Tool\_Chain\_Getting\_Started\_Guide* [1]. The Vinculum-II tool-chain consists of a compiler, assembler, linker and debugger encapsulated in a graphical interface (the IDE) and is used to develop customised firmware for VNC2 [2].

## 2.1 Hardware Configuration

For the purpose of this application note, disconnect is detected on GPIO Port B pin 0 which is routed to IOBUS8 on the V2Eval Board [7]. A jumper wire is connected from VBUS Enable on the VII Slave JP4 to physically detect the VBUS from the USB Host.



Figure 1: Connecting VBUS Enable to IOBUS8



### 2.2 Firmware Configuration

#### 2.2.1 IOMux Configuration

Signals on VNC2 must be configured using the IOMux. In addition, FTDI provides an IOMux configuration utility, as part of the IDE, to aid with configuring IOMux signals. For more information on IOMux and the IOMux configuration utility, see [3] and [4].

For this implementation using a 64-pin VNC2, Pin 19 is defined as an input and is controlled by GPIO Port B 0. The following source code performs this configuration, and sets the IO cell characteristics for the pin. See [5] for more details.

#### 2.2.2 GPIO Configuration

The VNC2 GPIO driver supports configurable interrupts. GPIO Port B is configured to interrupt when a state change occurs on pin 0. The following source code shows how to setup interrupt 0 on GPIO Port B to interrupt on the negative edge of the signal on Pin 0:

```
#define GPIOB
                 2
                      // user-defined device number
VOS HANDLE hGpioB;
// Open GPIO port B
hGpioB = vos_dev_open(GPIOB);
// Set GPIO mask
gpio iocb.ioctl code = VOS IOCTL GPIO SET MASK;
gpio_iocb.value = 0x00; // all input
vos_dev_ioctl(hGpioB, &gpio_iocb);
// configure interrupt for GPIO B 0 for falling edge
gpio_iocb.ioctl_code = VOS_IOCTL_GPIO_SET_PROG_INT0_PIN;
gpio iocb.value = GPIO PIN 0;
vos dev ioctl(hGpioB, &gpio iocb);
gpio iocb.ioctl code = VOS IOCTL GPIO SET PROG INTO MODE;
gpio_iocb.value = GPIO_INT_ON NEG EDGE;
vos dev ioctl(hGpioB, &gpio iocb);
```

```
vos_enable_interrupts(VOS_GPIO_INT_IEN);
```



#### 2.2.3 Monitoring the GPIO Input

An application thread is dedicated to monitoring the GPIO Port B 0 input signal. The thread blocks on a call to VOS\_IOCTL\_GPIO\_WAIT\_ON\_INTO. When the USB Slave device is disconnected from the bus, the change in state of VBUS Enable causes an interrupt on GPIO Port B 0, and the thread is unblocked as the IOCTL completes.

The thread sends a VOS\_IOCTL\_USBSLAVE\_DISCONNECT to the USB Slave driver to reset the USB Slave port. In this state, it is ready to be reconnected.

Finally, the thread calls VOS\_IOCTL\_GPIO\_SET\_PROG\_INTO\_MODE to re-enable the interrupt.

Note that the thread must be of sufficiently high priority that it will get to run when the GPIO interrupt has fired, and not be starved of CPU time by other higher priority threads. Since this thread will normally be blocked, it is safe to make this thread the highest priority in the application.

An implementation of a monitor thread is shown in the following source code:

```
void monitor usb disconnect(void) {
   gpio ioctl cb t gpio iocb;
   usbslave_ioctl_cb_t iocb;
   while (1) {
        // GPIO interrupt is configured - wait on detecting a disconnect
        gpio iocb.ioctl code = VOS IOCTL GPIO WAIT ON INTO;
        vos dev ioctl(hGpioB, &gpio iocb);
        // if we get here, we've been disconnected from the host!
        // put our slave back in a known state ready to be re-connected
        iocb.ioctl code = VOS IOCTL USBSLAVE DISCONNECT;
        iocb.set = (void *) 0;
        vos_dev_ioctl(hA,&iocb);
        // re-enable GPIO interrupt
        gpio iocb.ioctl code = VOS IOCTL GPIO SET PROG INTO MODE;
        gpio_iocb.value = GPIO_INT_ON_NEG_EDGE;
       vos_dev_ioctl(hGpioB, &gpio_iocb);
    }
}
```



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## **4** Appendix A – References

#### **Document References**

- [1] FTDI Application Note 142, *Vinculum-II\_Tool\_Chain\_Getting\_Started\_Guide*, FTDI, 2010.
- [2] FTDI Application Note 151, Vinculum-II User Guide, FTDI, 2010.
- [3] FTDI Application Note 139, *IO\_MUX Explained*, FTDI, 2010.
- [4] FTDI Application Note 144, *Vinculum-II\_IO\_Mux\_Config\_Utility\_User\_Guide*, FTDI, 2010.
- [5] FTDI Application Note 137, Vinculum-II IO Cell Description, FTDI, 2010.
- [6] FTDI Datasheet, Vinculum II Embedded Dual USB Host Controller IC, Version 1.2, FTDI, 2010.
- [7] FTDI Datasheet, V2-EVAL Vinculum II Evaluation Board, FTDI, 2010.

| Terms  | Description  |  |
|--------|--|--|
| IOMux  | Input Output Multiplexer – Used to configure pin selection on different package types of the VNC2.   |  |
| V2EVAL | Vinculum II Evaluation Board- Customer evaluation board for the VNC2 allowing prototype development. |  |
| VOS    | Vinculum Operating System  |  |
| IDE    | Integrated Development Environment   |  |
| VNC2   | Vinculum II  |  |

#### **Acronyms and Abbreviations**



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## 5 Appendix B – Revision History

| Revision | Changes         | Date       |
|----------|-----------------|------------|
| 1.0      | Initial Release | 2010-11-30 |